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APPLICATION NUMBER	FILING OR 371(c) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
10/565,551	09/11/2006	Ingrid M. Verbauwhede	UCLARF.003NP

CONFIRMATION NO. 2110

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Title: Dynamic and differential cmos logic with signal-independent power consumption to withstand differential power analysis

Publication No. US-2007-0057698-A1

Publication Date: 03/15/2007

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